

IN THE SPECIFICATION

At page 15, line 6, after "data and clock signals" insert:

-- In accordance with the invention, two of the clock phases are selected to be 180 degrees out of phase with the third clock phase, plus or minus a parameter M . The data signal is sampled at each of the three or more clock phases and a phase selection signal is generated based on a truth table. The state of the data signal in a previous cycle may further be included in the truth table. --